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Karen Vertz	(Depositor's name)
<i>Karen Vertz</i>	(Signature)
1-28-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/051,639	01/18/2002	Craig T. Salling	TI-32536	8729

TITLE OF INVENTION: ESD IMPROVEMENT BY A VERTICAL BIPOLAR TRANSISTOR WITH LOW BREAKDOWN VOLTAGE AND HIGH BETA

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	04/12/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LANDAU, MATTHEW C	2815	257-362000

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1 W. James Brady, III
2 Frederick J. Telecky, Jr.
3 _____

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Gary C. Honeycutt
Gary C. Honeycutt, Reg. No. 20,250

(Date)

1-28-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Craig T. Salling, et al.

Docket Number: TI-32536

Serial No.: 10/051,639

Art Unit: 2815

Filed: 01/18/02

Examiner: Matthew C. Landau

For: ESD Improvement by a Vertical Bipolar Transistor with
Low Breakdown Voltage and High Beta

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NAME OF INVENTOR(S): Craig T. Salling, et al.	
TITLE OF INVENTION: ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage and High Beta	
TI FILE NO.: TI-32536	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1-28-04 DUE: 4-12-04 ATTY/SECY: GCH/kjv	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/051,639 Filing Date: 01/18/02	

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